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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Gopakumar Parameswaran, Cuong C. Ly, Douglas L. Yanagawa, Mark N. Yamashita and Yuval Bachar

Assignee: Cisco Technology, Inc.

Title: SYSTEM AND METHOD FOR REDUCING CROSSTALK BETWEEN VIAS IN A PRINTED CIRCUIT BOARD

Application No.: 10/727,381

Filing Date: December 4, 2003

Examiner: Jeremy C. Norris

Group Art Unit: 2841

Docket No.: CIS0203US

Austin, Texas
November 20, 2006

MAIL STOP AF
COMMISSIONER FOR PATENTS
P. O. BOX 1450
ALEXANDRIA, VA 22313-1450

RESPONSE TO FINAL OFFICE ACTION

Dear Sir:

This paper is responsive to the Final Office Action dated September 20, 2006, having a shortened statutory period expiring on December 20, 2006. Further examination and reconsideration are respectfully requested in view of the amendments and remarks set forth below.

No Amendments to the Specification or Drawings in this paper.

A listing of the claims begins on page 2 of this paper.

Remarks begin on page 12 of this paper.